Notice of Allowability	Application No.	Applicant(s)	(and)
	10/785,062	TAKASU, HIDEKI	
	Examiner	Art Unit	
	Dean O. Takaoka	2817	
The MAILING DATE of this communication appearance All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT Report of the Office or upon petition by the applicant. See 37 CFR 1.313  1.  This communication is responsive to Applicant's amendment	(OR REMAINS) CLOSED in this apply or other appropriate communication IGHTS. This application is subject to 3 and MPEP 1308.	plication. If not included will be mailed in due co	ourse. THIS
2. 🔀 The allowed claim(s) is/are <u>1,3 and 4</u> .			
3.			
Attachment(s)  1. ☐ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 10/28/05  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5.  Notice of Informal P 6.  Interview Summary Paper No./Mail Dat 08), 7.  Examiner's Amendr 8.  Examiner's Stateme	(PTO-413), te nent/Comment	

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## **DETAILED ACTION**

## Allowable Subject Matter

Claims 1, 3 and 4 are allowed.

Takayama shows a high frequency switch comprising a single input and single output terminal, further including first thru third diodes and first and second resistors but does not teach or suggest capacitors connected to the ends of the first and third diodes controls the on/off function or where the DC bias circuits connected thereto in order to apply the DC bias (claim 1); or a plurality of outputs (claim 3). The selection switches of Takayama connected to the diodes appear to be controlled independently of any associated capacitor.

Nelson shows a pin diode switch including first thru third diodes and first and second resistors but does not teach or suggest capacitors connected to the ends of the first and third diodes controls the on/off function or where the DC bias circuits connected thereto in order to apply the DC bias; or a plurality of first and second resistors provided in a plurality of terminated lines (claim 3). Like Takayama above, the bias sources of Nelson connected to the diodes appear to be controlled independently of any associated capacitor where Nelson only discloses the functionality of the  $\lambda/4$  chokes blocking RF, nor would it be obvious to combine the prior art of record thus the claims are allowable.

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Dean O. Takaoka whose telephone number is (571) 272-1772. The examiner can normally be reached on 8:30a - 5:00p Mon - Fri.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on (571) 272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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November 8, 2005



OBLON, SPIVAK, et al
Docket No: 249253US2
Inventor: Hideki TAKASU
Serial No: 10/785,062
Response to Office Action dated: June 28, 2005
Replacement Sheet

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FIG. 3
BACKGROUND ART